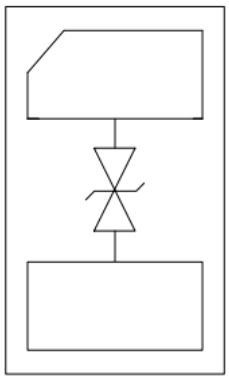
Description

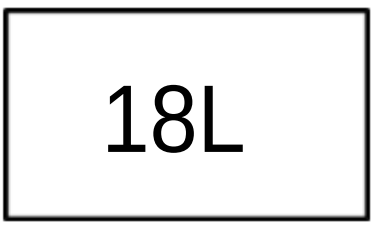
The JLE180BLD2-2 is a 18V bi-directional TVS diode, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive data and power line. The JLE180BLD2-2 complies with the IEC 61000-4-2 (ESD) with ±30 kV air and ±30 kV contact discharge. It is assembled into an ultra-small lead-free DFN1006-2 package. The small size and high ESD surge protection make JLE180BLD2-2 an ideal choice to protect cell phone, digital cameras, audio players and many other portable applications.

Circuit Diagram



Circuit and Pin Schematic

Marking Diagram



Transparent top view

18L:Device Marking Code

Features

* 300W peak pulse power (8/20μs)
* Low leakage：nA level
* Low operating voltage: 18V
* Ultra low clamping voltage
* One power line protects
* Complies with following standards:
  – IEC 61000-4-2 (ESD) immunity test
    Air discharge: ±30kV
    Contact discharge: ±30kV
  – IEC61000-4-5 (Lightning) 7A (8/20μs)
* RoHS Compliant
* Package: DFN1006-2

Applications

* Cellular Handsets and Accessories
* Personal Digital Assistants
* Notebooks and Handhelds
* Portable Instrumentation
* Digital Cameras
* Peripherals
* Audio Players

Ordering Information

Part Number	Packaging	Reel Size
JLE180BLD2-2	10000/Tape & Reel	7 inch

Absolute Maximum Ratings (T_A=25°C unless otherwise specified)

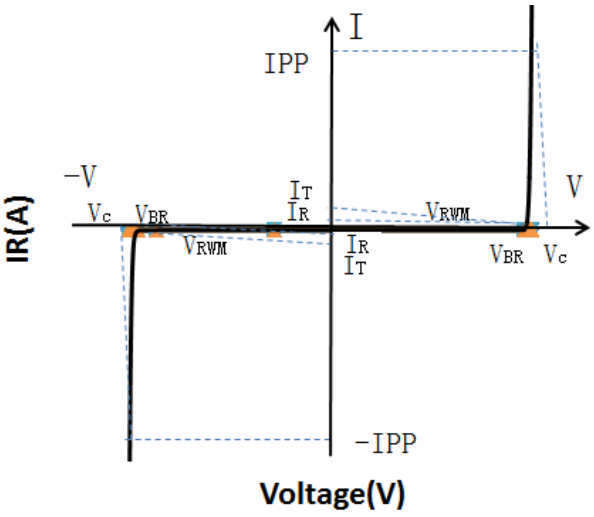
Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20μs)	Ppk	300	W
Peak Pulse Current (8/20μs)	IPP	7	A
ESD per IEC 61000−4−2 (Air) ESD per IEC 61000−4−2 (Contact)	VESD	±30 ±30	kV
Operating Temperature Range	TJ	−55to +125	°C
Storage Temperature Range	Tstg	−55 to +150	°C

Electrical Characteristics (T_A=25°C unless otherwise specified)

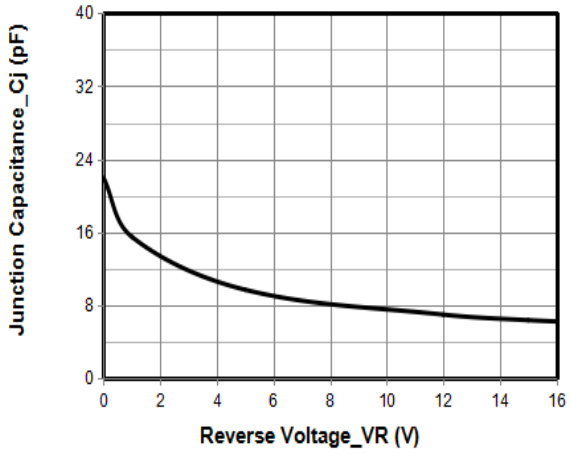
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Reverse Working Voltage	V_{RWM}				18	V
Breakdown Voltage	V_{BR}	I_T = 1mA	20			V
Reverse Leakage Current	I_R	V_{RWM} = 18V			0.2	μA
Clamping Voltage	Vc	I_{PP} = 1A (8 x 20μs pulse)			24	V
Clamping Voltage	Vc	I_{PP} =7A (8 x 20μs pulse)			42	V
Junction Capacitance	C_J	VR = 0V, f = 1MHz		20		pF

Portion Electronics Parameter

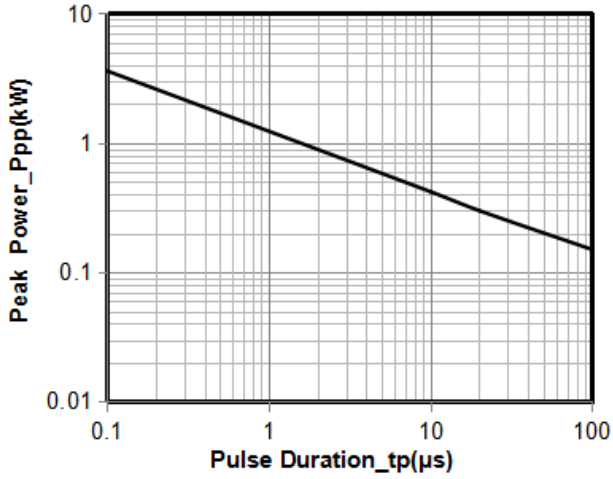
Symbol	Parameter
I_T	Test Current
I_{PP}	Maximum Reverse Peak Pulse Current
Vc	Clamping Voltage @Ic



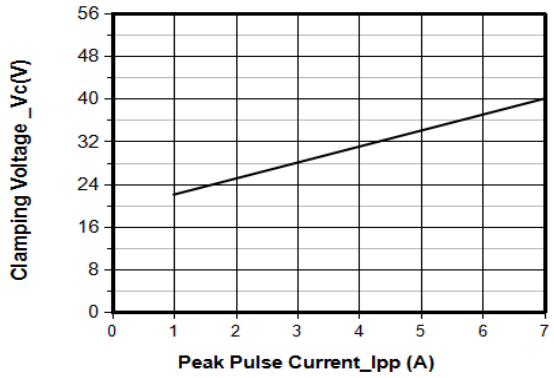
Typical Performance Characteristics (T_A=25°C unless otherwise Specified)



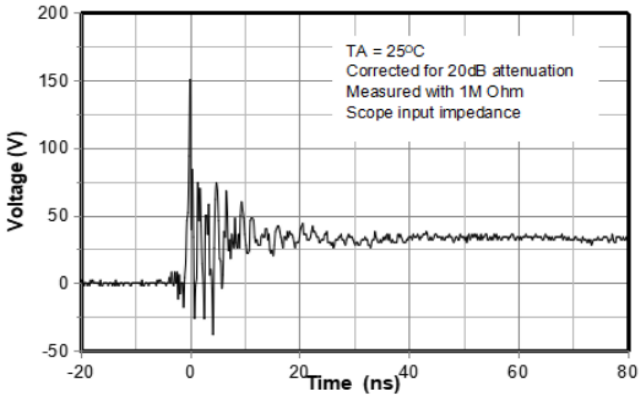
Junction Capacitance vs. Reverse Voltage



Clamping Voltage vs. Peak Pulse Current

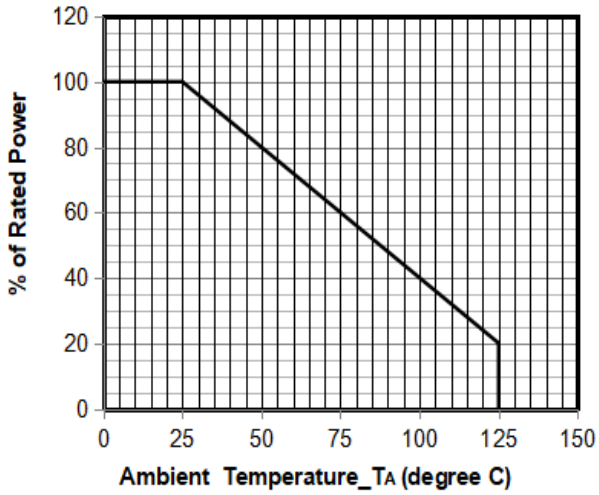


Peak Pulse Power vs. Pulse Time

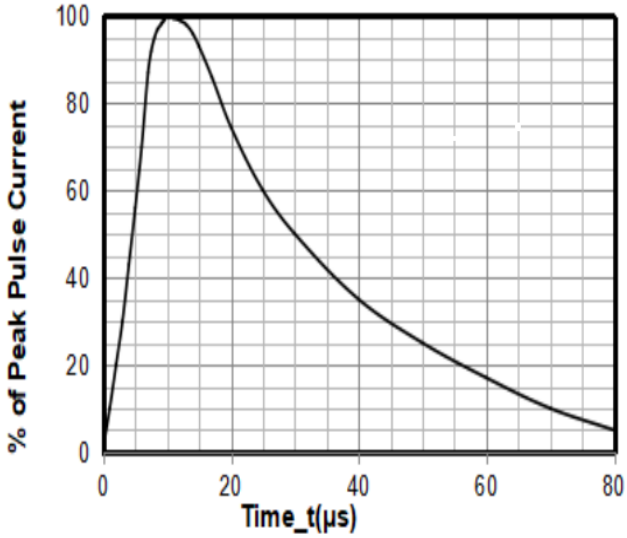


ESD Clamping Voltage

8 kV Contact per IEC61000-4-2

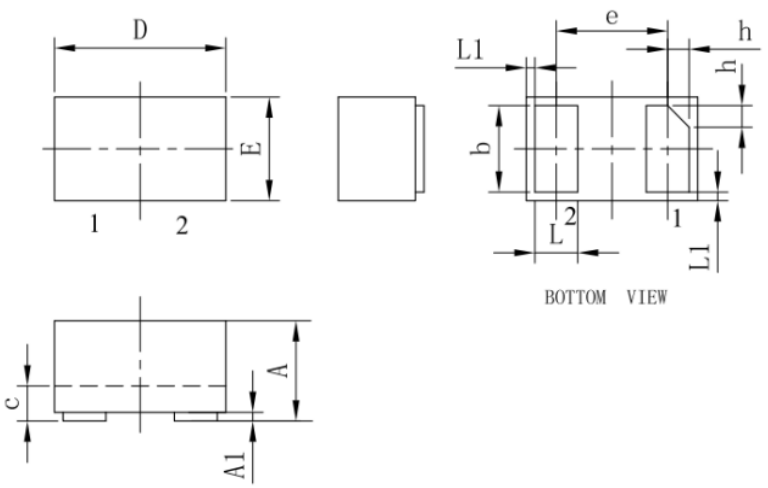


Power Derating Curve



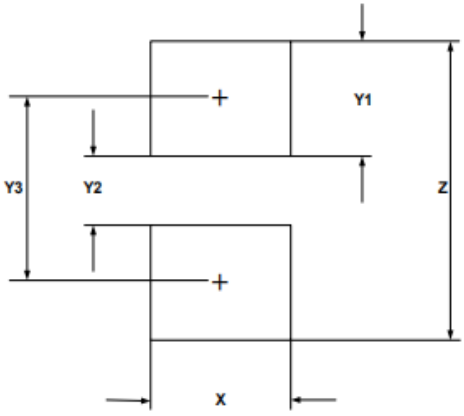
8 X 20µs Pulse Waveform

DFN1006-2 Package Outline Drawing (Dimensions in millimeters)



SYM	DIMENSIONS					
	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.45	0.50	0.55	0.018	0.020	0.022
A1	0.00	0.02	0.05	0.000	0.001	0.002
b	0.45	0.50	0.55	0.018	0.020	0.022
c	0.12	0.15	0.18	0.005	0.006	0.007
D	0.95	1.00	1.05	0.037	0.039	0.041
e	0.65 BSC			0.026 BSC		
E	0.55	0.60	0.65	0.022	0.024	0.026
L	0.20	0.25	0.30	0.008	0.010	0.012
L1	0.05REF			0.002REF		
h	0.07	0.12	0.17	0.003	0.005	0.007

Suggested Land Pattern



SYM	DIMENSIONS	
	MILLIMETERS	INCHES
X	0.60	0.024
Y1	0.50	0.020
Y2	0.30	0.012
Y3	0.80	0.032
Z	1.30	0.052

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