



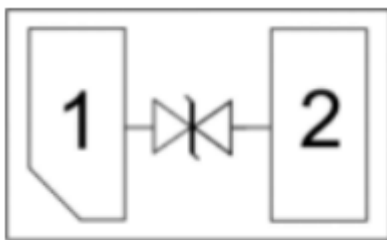
Description

The JLE05BUD2-2L is a bi-directional TVS diode, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive data and power line. The JLE05BUD2-2L complies with the IEC 61000-4-2 (ESD) with ± 25 kV air and ± 22 kV contact discharge. It is assembled into an ultra-small 1.0x0.6x0.5mm lead-free DFN package. The small size and high ESD surge protection make JLE05BUD2-2L an ideal choice to protect cell phone, digital cameras, audio players and many other portable applications.

Features

- * 40W peak pulse power (8/20 μ s)
- * Low leakage:nA level
- * Operating voltage: 5V
- * Low clamping voltage
- * One power line protects
- * Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
 - Air discharge: ± 25 kV
 - Contact discharge: ± 22 kV
 - IEC61000-4-5 (Lightning) 3A (8/20 μ s)
- * RoHS Compliant
- * Package: DFN1006-2

Circuit Diagram

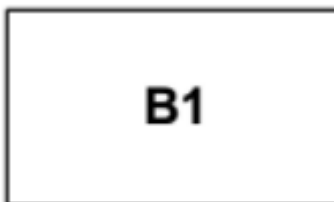


Circuit and Pin Schematic

Applications

- * Notebooks and Handhelds
- * Peripherals
- * Personal Digital Assistants
- * Cellular Handsets and Accessories
- * Portable Instrumentation
- * Audio Players
- * Keypads, Side Keys, LCD Displays

Marking Diagram



Transparent top view

B1:Device Marking Code

Ordering Information

Part Number	Packaging	Reel Size
JLE05BUD2-2L	10000/Tape & Reel	7 inch



JLE05BUD2-2L

Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise specified)

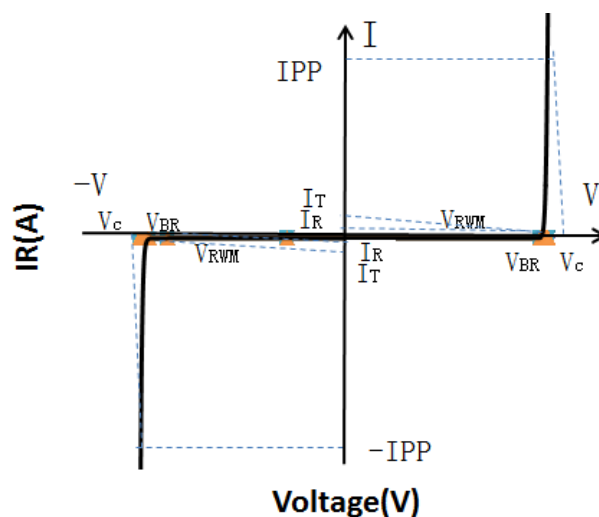
Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20 μs)	Ppk	40	W
Peak Pulse Current (8/20 μs)	IPP	3	A
ESD per IEC 61000-4-2 (Air)	VESD	± 25	kV
ESD per IEC 61000-4-2 (Contact)		± 22	
Operating Temperature Range	TJ	-55to +125	$^\circ\text{C}$
Storage Temperature Range	Tstg	-55 to +150	$^\circ\text{C}$

Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Reverse Working Voltage	V_{RWM}	Pin 1 to Pin 2 or Pin 2 to Pin 1			5	V
Breakdown Voltage	V_{BR}	$I_T = 1\text{mA}$, pin 1 to pin 2 or Pin 2 to Pin 1	6		8	V
Reverse Leakage Current	I_R	$V_{RWM} = 5\text{V}$, pin 1 to pin 2 or Pin 2 to Pin 1			0.2	μA
Clamping Voltage	V_C	$I_{PP} = 1\text{A}$ (8 x 20 μs pulse), pin 1 to pin 2 or Pin 2 to Pin 1			8	V
Clamping Voltage	V_C	$I_{PP} = 3\text{A}$ (8 x 20 μs pulse), pin 1 to pin 2 or Pin 2 to Pin 1			12	V
Junction Capacitance	C_J	$V_R = 0\text{V}$, $f = 1\text{MHz}$		5.5		pF

Portion Electronics Parameter

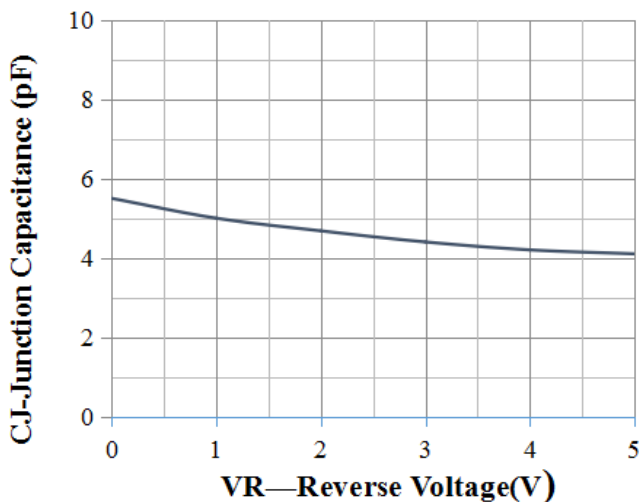
Symbol	Parameter
I_T	Test Current
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_C



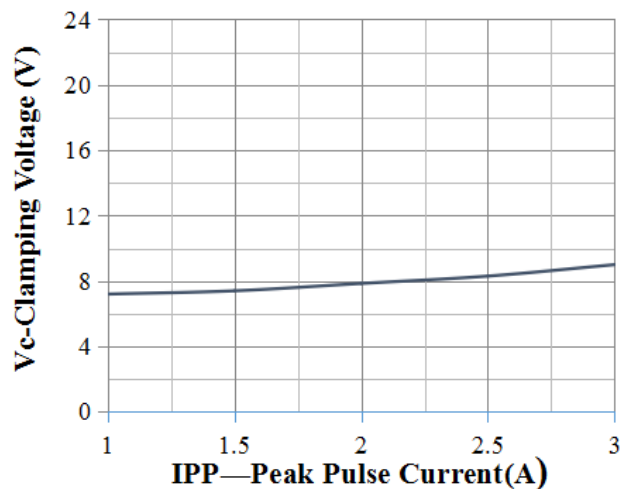


JLE05BUD2-2L

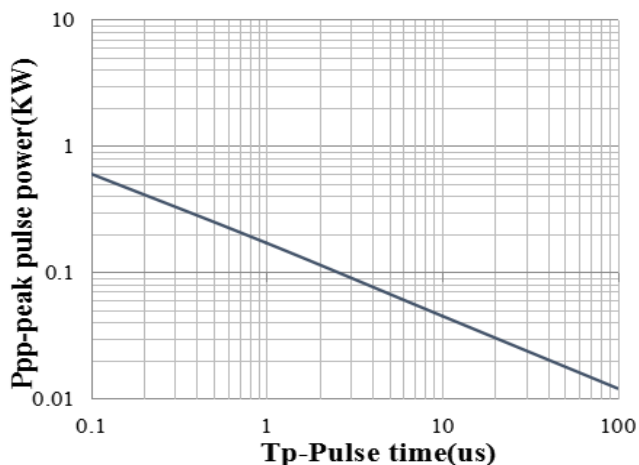
Typical Performance Characteristics ($T_A=25^\circ\text{C}$ unless otherwise Specified)



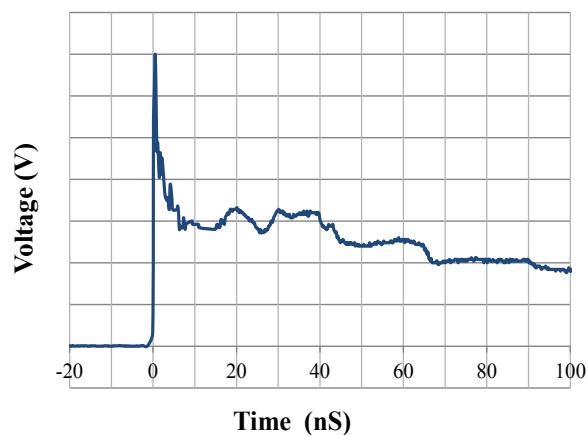
Junction Capacitance vs. Reverse Voltage



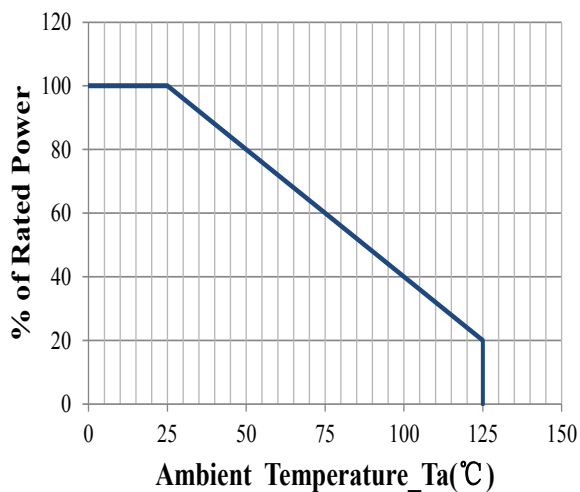
Clamping Voltage vs. Peak Pulse Current



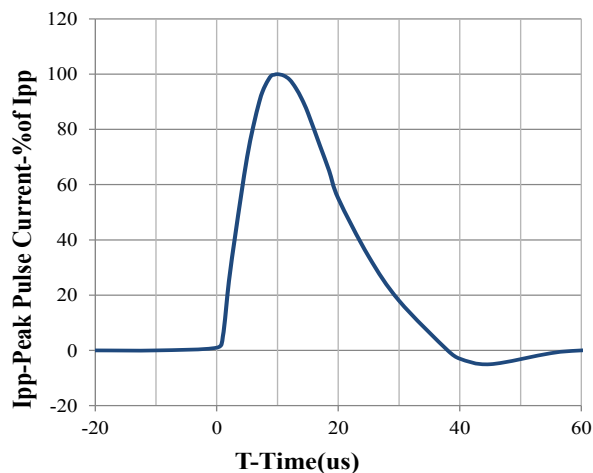
Peak Pulse Power vs. Pulse Time



IEC61000-4-2 Pulse Waveform



Power Derating Curve

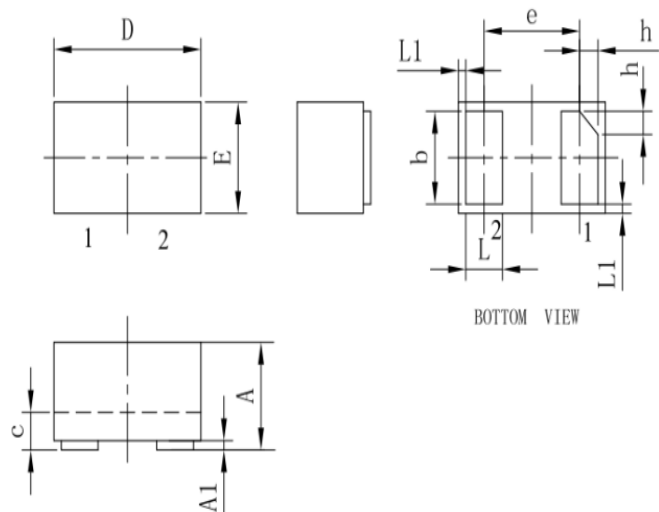


8 X 20us Pulse Waveform



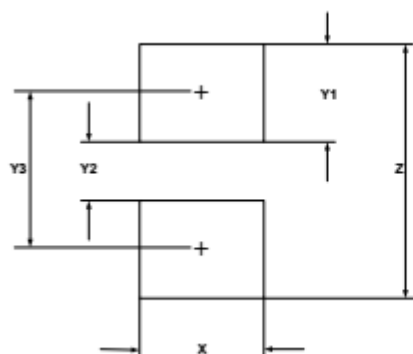
JLE05BUD2-2L

DFN1006-2 Package Outline Drawing (Dimensions in millimeters)



SYM	DIMENSIONS					
	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.45	0.50	0.55	0.018	0.020	0.022
A1	0.00	0.02	0.05	0.000	0.001	0.002
b	0.45	0.50	0.55	0.018	0.020	0.022
c	0.12	0.15	0.18	0.005	0.006	0.007
D	0.95	1.00	1.05	0.037	0.039	0.041
e	0.65 BSC			0.026 BSC		
E	0.55	0.60	0.65	0.022	0.024	0.026
L	0.20	0.25	0.30	0.008	0.010	0.012
L1	0.05REF			0.002REF		
h	0.07	0.12	0.17	0.003	0.005	0.007

Suggested Land Pattern



SYM	DIMENSIONS	
	MILLIMETERS	INCHES
X	0.60	0.024
Y1	0.50	0.020
Y2	0.30	0.012
Y3	0.80	0.032
Z	1.30	0.052

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