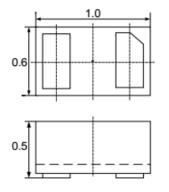


Description

The JLE12ULD2-2M is an uni-directional TVS diode, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive data and power line. The JLE12ULD2-2M complies with the IEC 61000-4-2 (ESD) with ± 30 kV air and ± 30 kV contact discharge. It is assembled into a small lead-free DFN1006-2 ($1.0 \times 0.6 \times 0.5$ mm) package. The small size and high ESD surge protection make JLE12ULD2-2M an ideal choice to protect cell phone, digital cameras, audio play- ers and many other portable applications.

Circuit Diagram



Circuit and Pin Schematic

Marking Diagram



Transparent top view

T3:Device Marking Code

Features

- * 200W peak pulse power (8/20µs)
- * Low leakage:nA
- * Operating voltage: 12V
- * Ultra low clamping voltage
- * Two power line protects
- * Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test

Air discharge: ±30kV

Contact discharge: ±30kV

- IEC61000-4-5 (Lightning) 9A (8/20µs)
- * RoHS Compliant
- Package: DFN1006-2

Applications

- * Fast-charge battery chargers
- * Power management system
- * Cellular Handsets and Accessories
- Personal Digital Assistants
- * Notebooks and Handhelds
- * Portable Instrumentation
- * Digital Cameras

Ordering Information

Part Number	Packaging	Reel Size
JLE12ULD2-2M	3000/Tape & Reel	7 inch



JLE12ULD2-2M

Absolute Maximum Ratings (T_A=25°C unless otherwise specified)

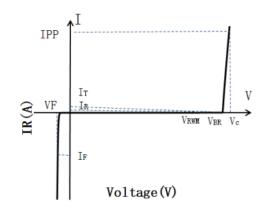
Parameter	Symbol	Value	Unit	
Peak Pulse Power (8/20µs)	Ppk	200	W	
Peak Pulse Current (8/20µs)	IPP	9	А	
ESD per IEC 61000-4-2 (Air)	VESD	±30	1-37	
ESD per IEC 61000-4-2 (Contact)	VESD	±30	kV	
Operating Temperature Range	TJ	-55 to +125	°C	
Storage Temperature Range	Tstg	-55 to +150	°C	

Electrical Characteristics (T_A=25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
Reverse Working Voltage	Vrwm				12	V
Breakdown Voltage	Vbr	$I_T = 1mA$	13.3		17.8	V
Reverse Leakage Current	I _R	$V_{RWM} = 12V$			0.2	μΑ
Clamping Voltage	Vc	$I_{PP} = 1A (8 \times 20 \mu s \text{ pulse})$			19	V
Clamping Voltage	Vc	IPP = 9A (8 x 20 μ s pulse)			25	V
Junction Capacitance	Сл	VR =0V,f=1MHz			60	pF

Portion Electronics Parameter

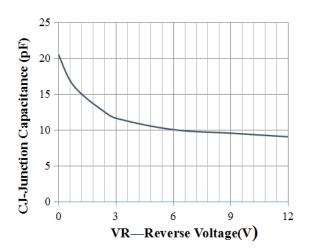
Symbol	Parameter			
Іт	Test Current			
Ірр	Maximum Reverse Peak Pulse Current			
Vc	Clamping Voltage @Ic			



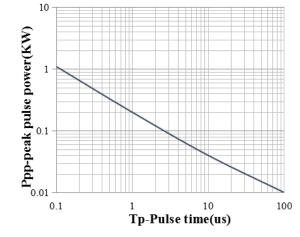
JLE12ULD2-2M



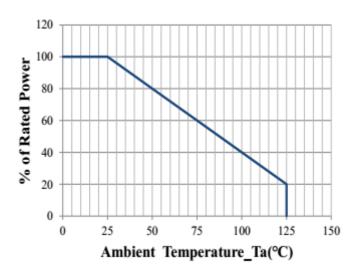




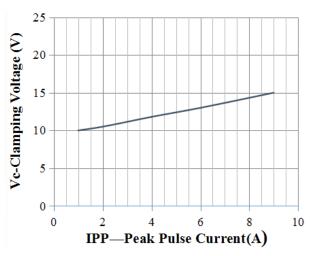
Junction Capacitance vs. Reverse Voltage



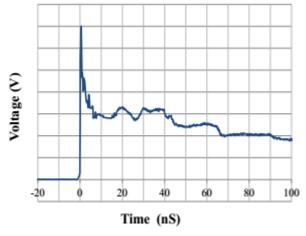
Peak Pulse Power vs. Pulse Time



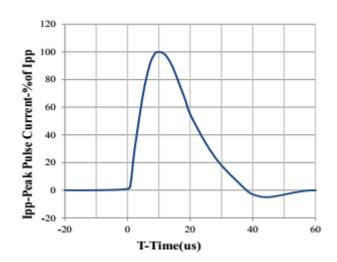
Power Derating Curve



Clamping Voltage vs. Peak Pulse Current



IEC61000-4-2 Pulse Waveform

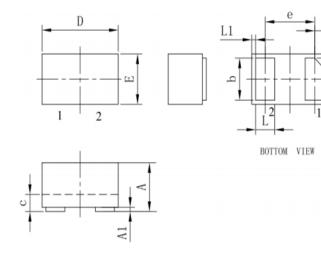


8 X 20us Pulse Waveform



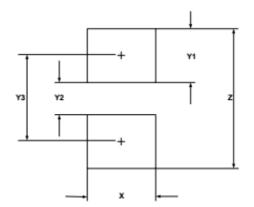
JLE12ULD2-2M

DFN1006-2 Package Outline Drawing



	DIMENSIONS					
	MILLIMETERS			INCHES		
SYM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.45	0.50	0.55	0.018	0.020	0.022
A1	0.00	0.02	0.05	0.000	0.001	0.002
b	0.45	0.50	0.55	0.018	0.020	0.022
с	0.12	0.15	0.18	0.005	0.006	0.007
D	0.95	1.00	1.05	0.037	0.039	0.041
е	0.65 BSC			0.026 BSC		
Е	0.55	0.60	0.65	0.022	0.024	0.026
L	0.20	0.25	0.30	0.008	0.010	0.012
L1	0.05REF			(0.002REF	=
h	0.07	0.12 0.17		0.003	0.005	0.007

Suggested Land Pattern



	DIMENSIONS				
SYM	MILLIMETERS	INCHES			
х	0.60	0.024			
Y1	0.50	0.020			
Y2	0.30	0.012			
Y3	0.80	0.032			
Z	1.30	0.052			

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