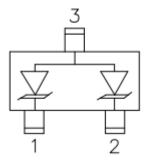


Description

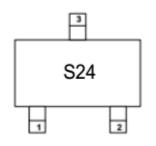
The JLE24ULT1-3 is a Uni-directional TVS diode, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive data and power line. The JLE24ULT1 -3 complies with the IEC 61000-4-2 (ESD) standard with ±30 kV air and ±30 kV contact discharge. It is assembled into an ultra-small lead-free SOT-23 package. The small size and high ESD surge protection make JLE24ULT1-3 an ideal choice to protect cell phone, digital cameras, audio players and many other portable applications.

Circuit Diagram



Circuit and Pin Schematic

Marking Diagram



Transparent top view

S24:Device Marking Code

Features

- * 300W peak pulse power (8/20µs)
- * Low leakage: nA level
- * Operating voltage: 24V
- * Low clamping voltage
- * Two power line protects
- * Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test

Air discharge: ±30kV

Contact discharge: ±30kV

- IEC61000-4-5 (Lightning) 5A (8/20µs)
- * RoHS Compliant
- * Package: SOT-23

Applications

- * Cellular Handsets and Accessories
- * Personal Digital Assistants
- * Notebooks and Handhelds
- * Digital Cameras
- * Set Top Box
- * Industrial Cotrols
- * Server and Deskop PC

Ordering Information

Part Number	Packaging	Reel Size
JLE24ULT1-3	3000/Tape & Reel	7 inch

JLE24ULT1-3

Absolute Maximum Ratings (T_A=25°C unless otherwise specified)

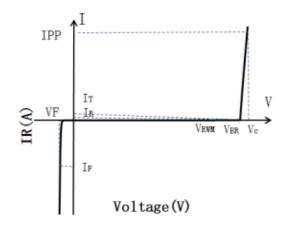
Parameter	Symbol	Value	Unit	
Peak Pulse Power (8/20µs)	Ppk	300	W	
Peak Pulse Current (8/20µs)	IPP	5	А	
ESD per IEC 61000-4-2 (Air)	VECD	±30	kV	
ESD per IEC 61000-4-2 (Contact)	VESD	±30		
Operating Temperature Range	TJ	-55to +125	°C	
Storage Temperature Range	Tstg	-55 to +150	°C	

Electrical Characteristics (T_A=25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
Reverse Working Voltage	Vrwm				24	V
Breakdown Voltage	Vbr	$I_T = 1 m A$	27			V
Reverse Leakage Current	I _R	$V_{RWM} = 24V$			0.2	μA
Clamping Voltage	Vc	IPP = $1A (8 \times 20 \mu \text{s pulse})$			40	V
Clamping Voltage	Vc	$I_{PP} = 5A (8 \times 20 \mu s \text{ pulse})$			60	V
Junction Capacitance	Сл	VR = 0V, $f = 1MHz$, Pin 1 to pin 3 or Pin 2 to Pin 3			50	pF
Junction Capacitance	Сл	VR = 0V, f = 1MHz,Pin 1 to pin 2			25	pF

Portion Electronics Parameter

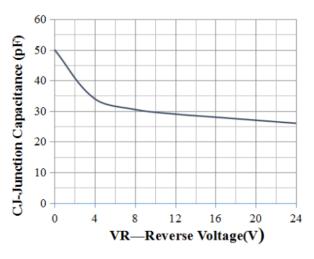
Symbol	Parameter		
Ιτ	Test Current		
Ірр	Maximum Reverse Peak Pulse Current		
Vc	Clamping Voltage @Ic		



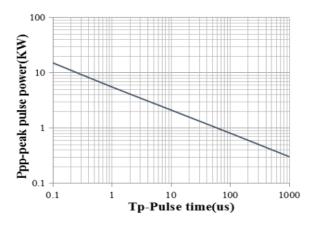
JLE24ULT1-3



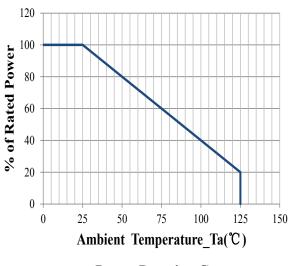
Typical Performance Characteristics (T_A=25°C unless otherwise Specified)



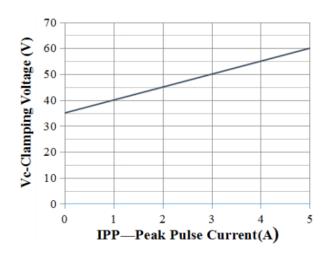
Junction Capacitance vs. Reverse Voltage



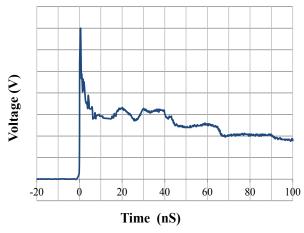
Peak Pulse Power vs. Pulse Time



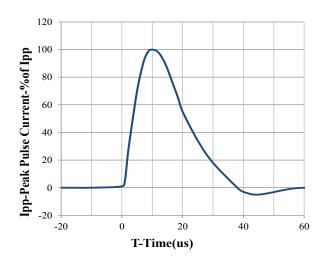
Power Derating Curve



Clamping Voltage vs. Peak Pulse Current



IEC61000-4-2 Pulse Waveform

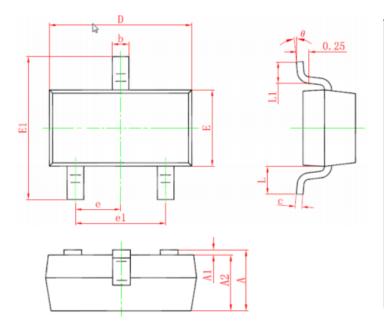


8 X 20us Pulse Waveform



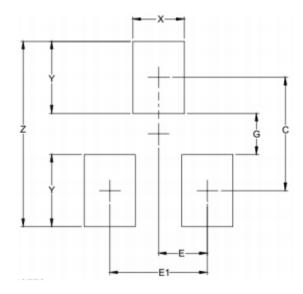
JLE24ULT1-3

SOT-23 Package Outline Drawing (Dimensions in millimeters)



	DIMENSIONS					
	MILLIMETERS		INCHES			
SYM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.90		1.15	0.035		0.045
A1	0.00		0.10	0.000		0.004
A2	0.90		1.05	0.035		0.041
b	0.30		0.50	0.012		0.020
с	0.08		0.15	0.003		0.006
D	2.80		3.00	0.110		0.118
E	1.20		1.40	0.047		0.055
E1	2.25		2.25	0.089		0.100
е	0.95TYP			0.037TYP		
e1	1.80		2.00	0.071		0.079
L	0.55REF			0.022REF		
L1	0.30		0.50	0.012		0.020
Θ	0°		8°	0°		8°

Suggested Land Pattern



SYM	DIMENSIONS			
	INCHES	MILLIMETERS		
С	(.087)	(2.20)		
E	.037	0.95		
E1	.075	1.90		
G	.031	0.80		
х	.039	1.00		
Y	.055	1.40		
Z	.141	3.60		

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