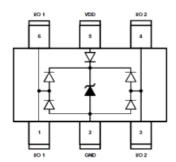
Description

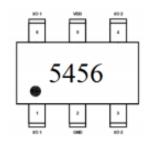
The JLE05URT5-6 is an ultra low capacitance TVS array, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, mak- ing this device an ideal solution for protecting voltage sensitive high-speed data lines. The JLE05URT5-6 has an ultra-low capacitance with a typical value at 0.3pF, and complies with the IEC 61000-4-2 (ESD) standard with ±15kV air and ±8kV contact discharge. It is assembled into a 6-pin lead-free SOT-563 package. The combination of small size, ultra low capacitance, and high ESD surge capability make it ideal for use in applications such as USB 3.0, multimedia, and other high speed ports.

Circuit Diagram



Circuit and Pin Schematic

Marking Diagram



Transparent top view

5456:Device Marking Code

Features

- * 100W peak pulse power (8/20µs)
- * Low leakage: nA level
- Operating voltage: 5V
- * Ultra low clamping voltage
- * Up to 4 data line and one power line protects
- * Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test

Air discharge: ±30kV

Contact discharge: ±30kV

- IEC61000-4-5 (Lightning) 5A (8/20μs)
- * RoHS Compliant
- * Package: SOT-563

Applications

- * USB 2.0 and USB 3.0 Ports
- USB OTG
- Digital Video Interface (DVI)
- * Monitor and Flat Panel Displays
- * Gigabit Ethernet
- * IEEE 1394 Firewire Ports
- Consumer products (STB,DVD,DSC,DVC)

Ordering Information

Part Number	Packaging	Reel Size	
JLE05URT5-6	3000/Tape & Reel	7 inch	



Absolute Maximum Ratings (T_A=25°C unless otherwise specified)

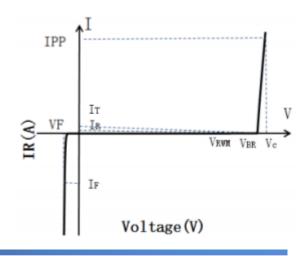
Parameter	Symbol	Value	Unit	
Peak Pulse Power (8/20μs)	Ppk	100	W	
Peak Pulse Current (8/20μs)	IPP	5	A	
ESD per IEC 61000-4-2 (Air)	VESD	±30	kV	
ESD per IEC 61000-4-2 (Contact)	VESD	±30		
Operating Temperature Range	TJ	-55to +125	°C	
Storage Temperature Range	Tstg	-55 to +150	°C	

Electrical Characteristics (T_A=25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
Reverse Working Voltage	VRWM	Any I/O pin to ground			5	V
Breakdown Voltage	VBR	I _T = 1mA,any I/O pin to ground	6			V
Reverse Leakage Current	I_R	$V_{RWM} = 5V$, any I/O pin to ground			0.5	μΑ
Clamping Voltage	Vc	IPP = 1A (8 x 20μs pulse),any I/O pin to ground			15	V
Clamping Voltage	Vc	IPP = 5A (8 x 20μs pulse),any I/O pin to ground			20	V
Junction Capacitance	CJ	VR = 0V, f = 1MHz,beween I/O pins		0.3	0.4	pF
Junction Capacitance	CJ	VR = 0V, $f = 1MHz$, any I/O pin to ground			0.8	pF

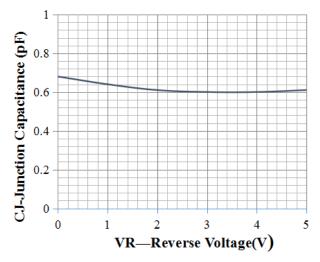
Portion Electronics Parameter

Symbol	Parameter
IT	Test Current
Ірр	Maximum Reverse Peak Pulse Current
Vc	Clamping Voltage @Ic

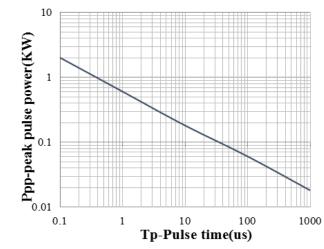




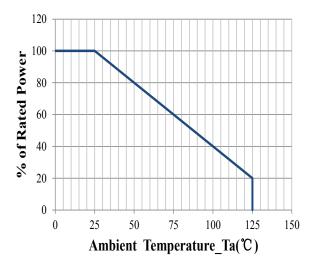
Typical Performance Characteristics (T_A=25°C unless otherwise Specified)



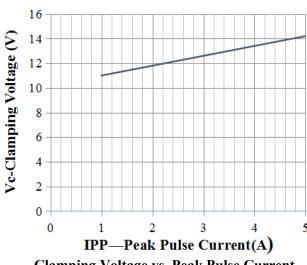
Junction Capacitance vs. Reverse Voltage



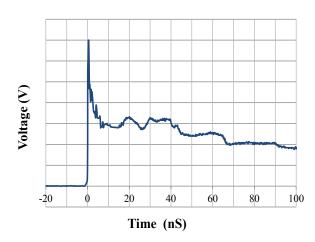
Peak Pulse Power vs. Pulse Time



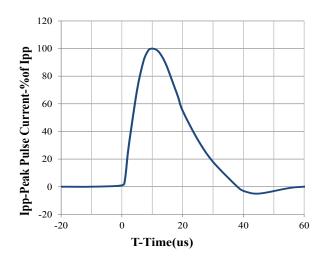
Power Derating Curve



Clamping Voltage vs. Peak Pulse Current



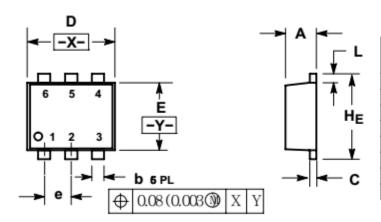
IEC61000-4-2 Pulse Waveform



8 X 20us Pulse Waveform

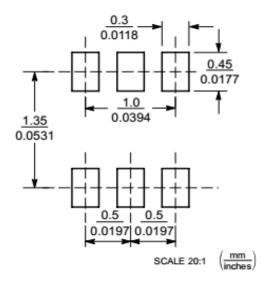


SOT-563 Package Outline Drawing (Dimensions in millimeters)



	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.50	0.55	0.60	0.020	0.021	0.023
b	0.17	0.22	0.27	0.007	0.009	0.011
С	0.08	0.12	0.18	0.003	0.005	0.007
D	1.50	1.60	1.70	0.059	0.062	0.066
E	1.10	1.20	1.30	0.043	0.047	0.051
е	0.5 BSC			(0.02 BS0	
L	0.10	0.20	0.30	0.004	0.008	0.012
HE	1.50	1.60	1.70	0.059	0.062	0.066

Suggested Land Pattern



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