



JLE05BUS5-2L

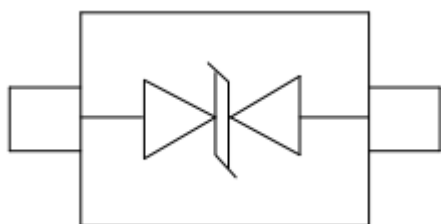
1-Line Bi-directional High Power TVS Diode

Jialan-Microelectronics

Description

The JLE05BUS5-2L is a bi-directional TVS diode, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive data and power line. The JLE05BUS5-2L complies with the IEC 61000-4-2 (ESD) standard with ± 15 kV air and ± 8 kV contact discharge. It is assembled into an ultra-small SOD-523 lead-free package. The small size and high ESD surge protection make JLE05BUS5-2L an ideal choice to protect cell phone, digital cameras, audio players and many other portable applications.

Circuit Diagram



Circuit and Pin Schematic

Marking Diagram



Transparent top view

5K:Device Marking Code

Features

- * Ultra low leakage: nA
- * Low operating voltage: 5V
- * Low clamping voltage
- * One power line protects
- * Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
 - Air discharge: ± 15 kV
 - Contact discharge: ± 8 kV
 - IEC61000-4-5 (Lightning) 2A (8/20 μ s)
- * Package: SOD-523
- * ROHS Compliant

Applications

- * Cellular Handsets and Accessories
- * Personal Digital Assistants
- * Notebooks and Handhelds
- * Portable Instrumentation
- * Digital Cameras
- * Peripherals
- * Audio Players
- * Keypads, Side Keys, LCD Displays, USB2.0

Ordering Information

Part Number	Packaging	Reel Size
JLE05BUS5-2L	3000/Tape & Reel	7 inch



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Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise specified)

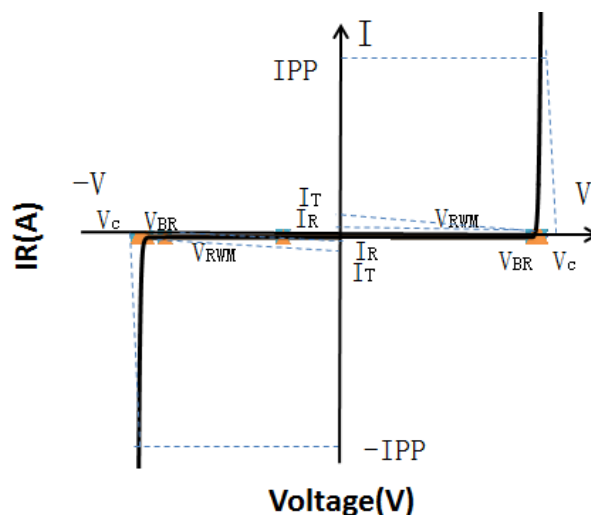
Parameter	Symbol	Value	Unit
ESD per IEC 61000-4-2 (Air)	VESD	± 30	kV
ESD per IEC 61000-4-2 (Contact)		± 30	
Operating Temperature Range	TJ	-55 to +125	$^\circ\text{C}$
Storage Temperature Range	Tstg	-55 to +150	$^\circ\text{C}$

Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Reverse Working Voltage	V_{RWM}				5	V
Breakdown Voltage	V_{BR}	$I_T = 1\text{mA}$	6	8	9	V
Reverse Leakage Current	I_R	$V_{RWM} = 5\text{V}$			200	nA
Clamping Voltage	V_C	$I_{PP} = 1\text{A}$ (8 x 20 μs pulse)			10	V
Junction Capacitance	C_J	$V_R = 0\text{V}$, $f = 1\text{MHz}$		2.5	3	pF

Portion Electronics Parameter

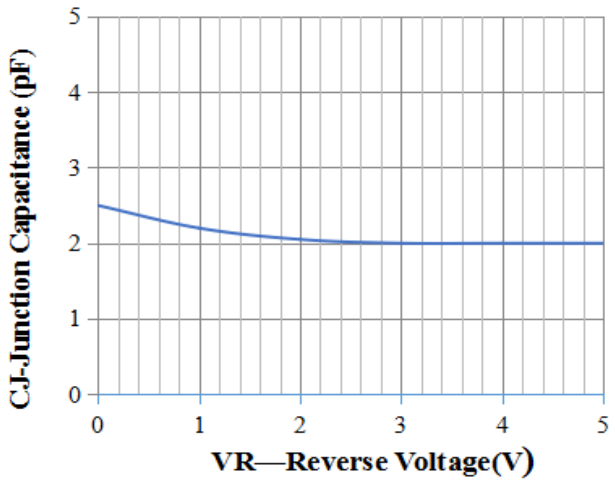
Symbol	Parameter
I_T	Test Current
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_C



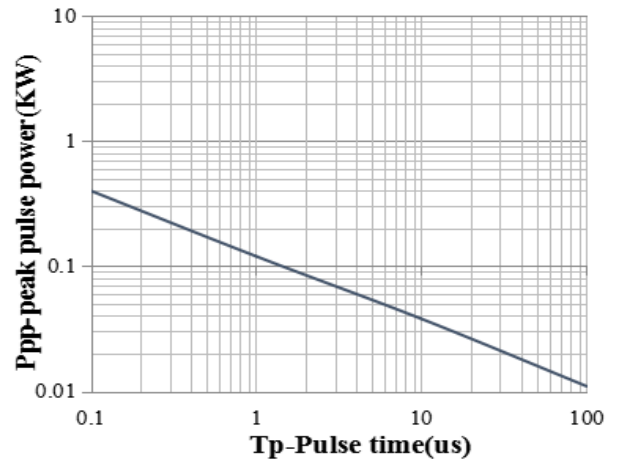


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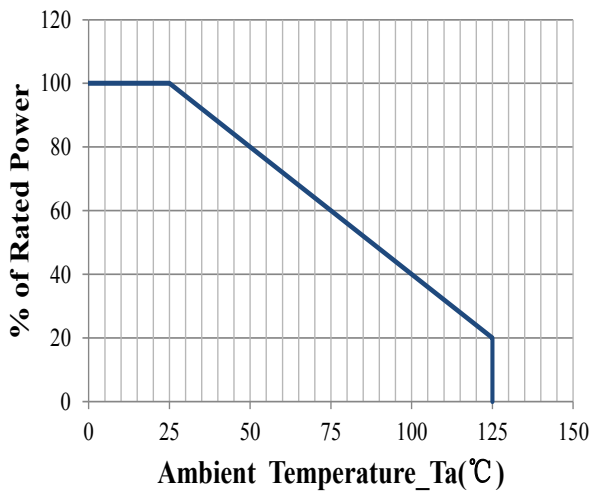
Typical Performance Characteristics ($T_A=25^\circ\text{C}$ unless otherwise Specified)



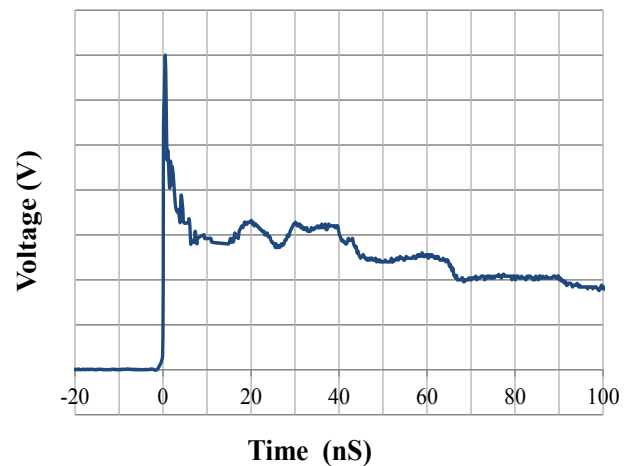
Junction Capacitance vs. Reverse Voltage



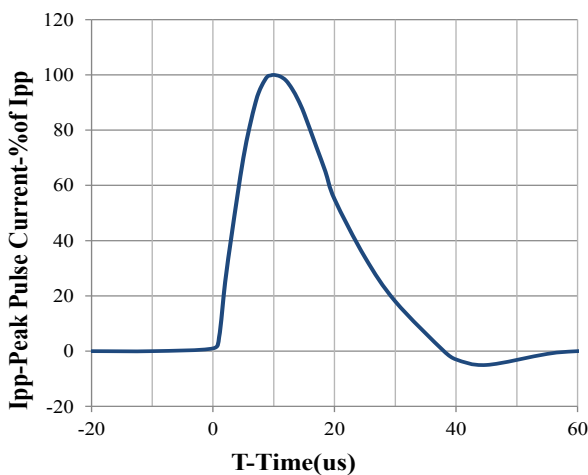
Peak Pulse Power vs. Pulse Time



Power Derating Curve



IEC61000-4-2 Pulse Waveform

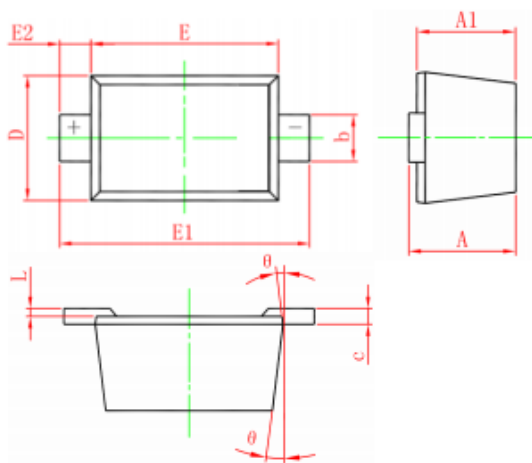


8 X 20us Pulse Waveform



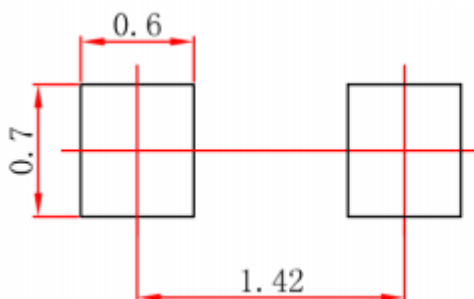
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SOD-523 Package Outline Drawing (Dimensions in millimeters)



SYM	DIMENSIONS					
	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.51	--	0.77	0.020	--	0.031
A1	0.50	--	0.70	0.020	--	0.028
b	0.25	--	0.35	0.010	--	0.014
c	0.08	--	0.15	0.003	--	0.006
D	0.75	--	0.85	0.030	--	0.033
E	1.10	--	1.30	0.043	--	0.051
E1	1.50	--	1.70	0.059		0.067
E2	0.20REF			0.008REF		
L	0.01	--	0.07	0.001	--	0.003
Θ	7° REF			7° REF		

Suggested Land Pattern



Unit: mm

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