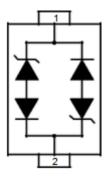


### Description

The JLE05BRS3-2 is a bi-directional TVS diode, utilizing leading monolithic silicon technology to provide fast re-sponse time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive high- speed data lines. The JLE05BRS3-2 has an ultra-low capacitance with a typical value at 0.5pF, and complies with the IEC 61000-4-2 (ESD) standard with  $\pm$ 30kV air and  $\pm$ 25kV contact discharge. It is assembled into a SOD-323 lead-free package. The small size, ultra low capaci-tance and high ESD surge protection make JLE05BRS3-2 an ideal choice to protect cell phone, digital video inter-faces and other high speed ports.

### **Circuit Diagram**



Circuit and Pin Schematic

# **Marking Diagram**



**Transparent top view** 2X:Device Marking Code

### Features

- \* 150W peak pulse power (8/20µs)
- \* Low leakage:nA level
- Operating voltage: 5V
- \* Ultra low clamping voltage
- \* One power line protects
- \* Complies with following standards:

- IEC 61000-4-2 (ESD) immunity test

Air discharge: ±30kV

Contact discharge: ±25kV

#### – IEC61000-4-5 (Lightning) 6A (8/20µs)

- \* RoHS Compliant
- Package: SOD-323

# **Applications**

- \* Fast-charge battery chargers
- \* Power management system
- \* Cellular Handsets and Accessories
- Personal Digital Assistants
- \* Notebooks and Handhelds
- \* Portable Instrumentation
- \* Digital Cameras

## **Ordering Information**

Part Number	Packaging	<b>Reel Size</b>	
JLE05BRS3 -2	3000/Tape & Reel	7 inch	1

# JLE05BRS3-2

# Absolute Maximum Ratings (T<sub>A</sub>=25°C unless otherwise specified)

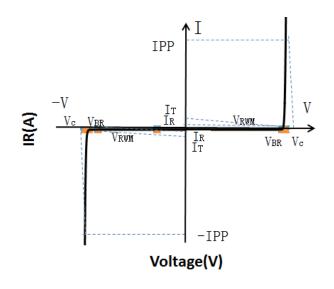
Parameter	Symbol	Value	Unit	
Peak Pulse Power (8/20µs)	Ppk	150	W	
Peak Pulse Current (8/20µs)	IPP	6	А	
ESD per IEC 61000-4-2 (Air)	VESD	±30	kV	
ESD per IEC 61000-4-2 (Contact)	VESD	±25	K V	
Operating Temperature Range	TJ	-55to +125	°C	
Storage Temperature Range	Tstg	-55 to +150	°C	

# Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
Reverse Working Voltage	Vrwm				5.0	V
Breakdown Voltage	Vbr	$I_T = 1mA$	6.5		9.5	V
Reverse Leakage Current	I <sub>R</sub>	$V_{RWM} = 5V$			0.2	μΑ
Clamping Voltage	Vc	IPP = $1A (8 \times 20 \mu s \text{ pulse})$			12	V
Clamping Voltage	Vc	$I_{PP} = 6A (8 \times 20 \mu s \text{ pulse})$			25	V
Junction Capacitance	Сл	VR = 0V, f = 1MHz		0.3	0.5	pF

# **Portion Electronics Parameter**

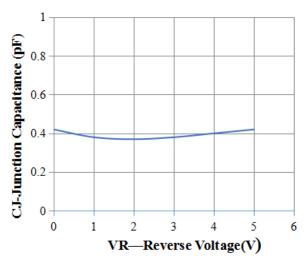
Symbol	Parameter
Іт	Test Current
Ірр	Maximum Reverse Peak Pulse Current
Vc	Clamping Voltage @Ic



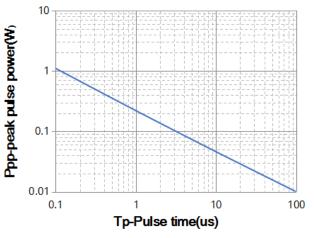
# JLE05BRS3-2



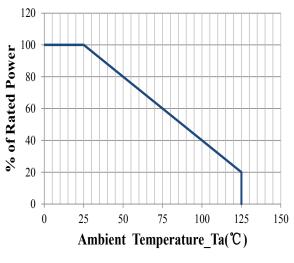




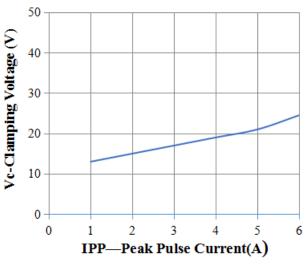
Junction Capacitance vs. Reverse Voltage



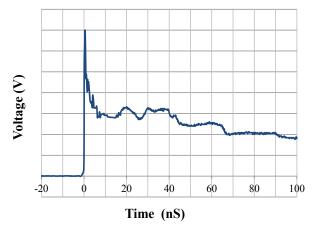
Peak Pulse Power vs. Pulse Time



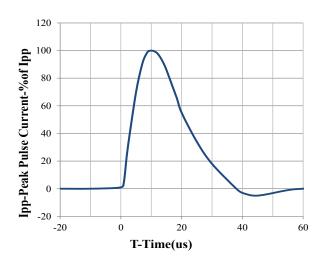
**Power Derating Curve** 



**Clamping Voltage vs. Peak Pulse Current** 



IEC61000-4-2 Pulse Waveform

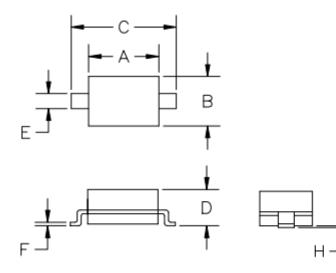


8 X 20us Pulse Waveform



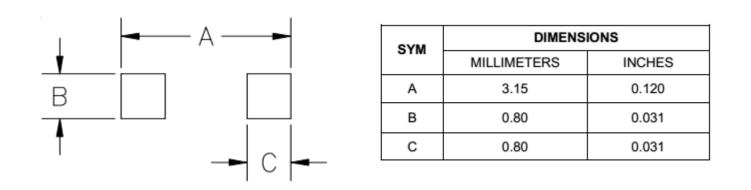
# JLE05BRS3-2

### SOD-323 Package Outline Drawing (Dimensions in millimeters)



	DIMENSIONS				
SYM	MILLIMETERS		INCHES		
••••	MIN	MAX	MIN	MAX	
Α	1.50	1.80	0.060	0.071	
В	1.20	1.40	0.045	0.054	
С	2.30	2.70	0.090	0.107	
D	-	1.10	-	0.043	
E	0.30	0.40	0.012	0.016	
F	0.10	0.25	0.004	0.010	
н	-	0.10	-	0.004	

## **Suggested Land Pattern**



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