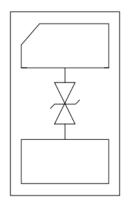


### Description

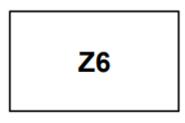
The JLE05BUD1-2H is a bi-directional TVS diode, utilizing leading monolithic silicon technology to provide fast response time, very low capacitance and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive data and power line. The JLE05BUD1-2H complies with the IEC 61000-4-2 (ESD) with  $\pm 15$ kV air and  $\pm 8$ kV contact discharge. It is assembled into an ultra- small lead-free package. The small size and very low capacitance make JLE05BUD1-2H an ideal choice to protect cell phone, digital cameras, audio players, data interface and many other portable applications.

### **Circuit Diagram**



Circuit and Pin Schematic

### **Marking Diagram**



#### Transparent top view

Z6:Device Marking Code

#### Features

- \* Very low capacitance: 2.5pF typical
- \* Low leakage: nA level
- \* Low operating voltage: 5V
- \* Ultra low clamping voltage
- \* One power line protects
- \* Complies with following standards:
  - IEC 61000-4-2 (ESD) immunity test

Air discharge: ±15kV

Contact discharge: ±8kV

- IEC61000-4-5 (Lightning) 2A (8/20µs)
- \* RoHS Compliant
- Package: DFN0603-2

#### **Applications**

- \* Cellular Handsets and Accessories
- Personal Digital Assistants
- \* Notebooks and Handhelds
- \* Portable Instrumentation
- \* Digital Cameras
- \* Peripherals
- Audio Players

### **Ordering Information**

Part Number	Packaging	Reel Size
JLE05BUD1-2H	10000/Tape & Reel	7 inch



# Absolute Maximum Ratings (T<sub>A</sub>=25°C unless otherwise specified)

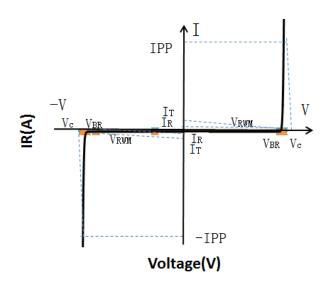
Parameter	Symbol	Value	Unit	
ESD per IEC 61000-4-2 (Air)	VESD	±15	kV	
ESD per IEC 61000-4-2 (Contact)	VESD	$\pm 8$		
Operating Temperature Range	TJ	-55to +125	°C	
Storage Temperature Range	Tstg	-55 to +150	°C	

# Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
Reverse Working Voltage	Vrwm				5	V
Breakdown Voltage	VBR	$I_T = 1mA$	6			V
Reverse Leakage Current	I <sub>R</sub>	$V_{RWM} = 5V$			0.2	μΑ
Clamping Voltage	Vc	IPP = $1A (8 \times 20 \mu s \text{ pulse})$			10	V
Junction Capacitance	Сл	VR = 0V, f = 1MHz		2.5	3	pF

### **Portion Electronics Parameter**

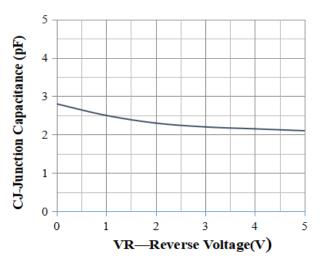
Symbol	Parameter	
Іт	Test Current	
Ірр	Maximum Reverse Peak Pulse Current	
Vc	Clamping Voltage @Ic	



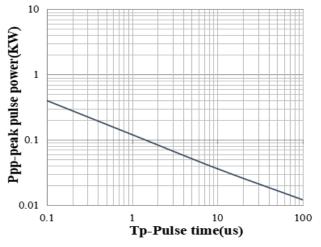


## JLE05BUD1-2H

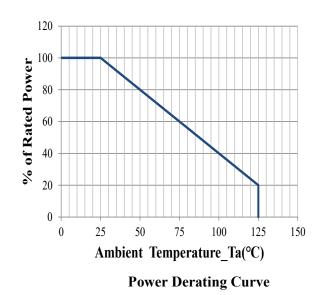
# Typical Performance Characteristics (T<sub>A</sub>=25°C unless otherwise Specified)

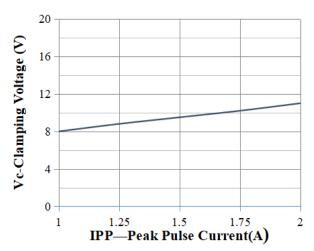


Junction Capacitance vs. Reverse Voltage

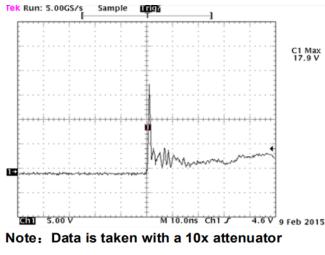


Peak Pulse Power vs. Pulse Time

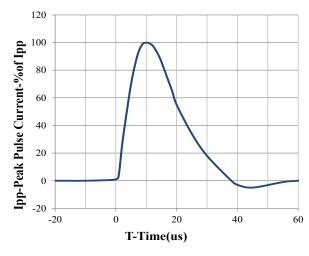




**Clamping Voltage vs. Peak Pulse Current** 



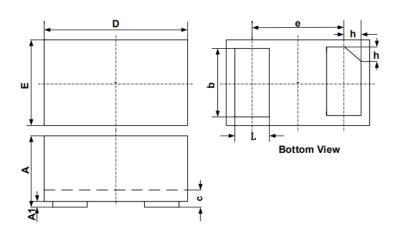
IEC61000-4-2 Pulse Waveform



8 X 20us Pulse Waveform

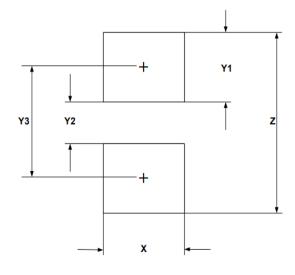
# JLE05BUD1-2H

### **DFN0603-2** Package Outline Drawing (Dimensions in millimeters)



	DIMENSIONS		
	MILLIMETERS		
SYM	MIN	NOM	MAX
Α	0.230		0.330
A1	0.000	0.020	0.050
b	0.215	0.245	0.275
С	0.120	0.150	0.180
D	0.550	0.600	0.650
е	0.355 BSC		
E	0.250	0.300	0.350
L	0.160	0.190	0.220
h		0.079 BSC	)

### **Suggested Land Pattern**



SYM	DIMENSIONS		
311	MILLIMETERS	INCHES	
х	0.30	0.012	
¥1	0.25	0.010	
Y2	0.15	0.006	
Y3	0.40	0.016	
z	0.65	0.026	

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