JLE05ULS9-2

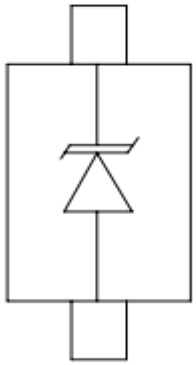
1-Line Uni-directional High Power TVS Diode

Jialan-Microelectronics

Description

The JLE05ULS9-2 is an uni-directional TVS diode, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive data and power line. The JLE05ULS9-2 complies with the IEC 61000-4-2 (ESD) with ±30kV air and ±30kV contact discharge. It is assembled into an ultra-small lead-free SOD-923 package. The small size and high ESD surge protection make JLE05ULS9-2 an ideal choice to protect cell phone, digital cameras, audio players and many other portable applications.

Circuit Diagram



SOD-923 (Top View)

Circuit and Pin Schematic

Marking Diagram

Transparent top view

59:Device Marking Code

Features

* 100W peak pulse power (8/20μs)
* Low leakage:nA level
* Operating voltage: 5V
* Ultra low clamping voltage
* One power line protects
* Complies with following standards:
  - IEC 61000-4-2 (ESD) immunity test
    Air discharge: ±30kV
    Contact discharge: ±30kV
  - IEC61000-4-5 (Lightning) 8A (8/20μs)
* RoHS Compliant
* Package: SOD-923

Applications

* Fast-charge battery chargers
* Power management system
* Cellular Handsets and Accessories
* Personal Digital Assistants
* Notebooks and Handhelds
* Portable Instrumentation
* Digital Cameras

Ordering Information

Part Number	Packaging	Reel Size
JLE05ULS9-2	8000/Tape & Reel	7 inch

Absolute Maximum Ratings (T_A=25°C unless otherwise specified)

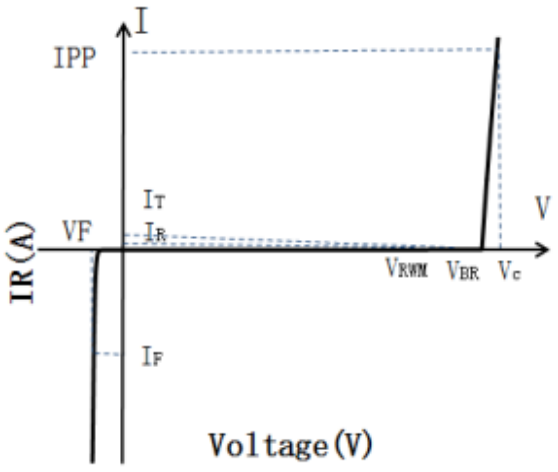
Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20μs)	Ppk	100	W
Peak Pulse Current (8/20μs)	IPP	8	A
ESD per IEC 61000−4−2 (Air) ESD per IEC 61000−4−2 (Contact)	VESD	±30 ±30	kV
Operating Temperature Range	TJ	−55 to +125	°C
Storage Temperature Range	Tstg	−55 to +150	°C

Electrical Characteristics (T_A=25°C unless otherwise specified)

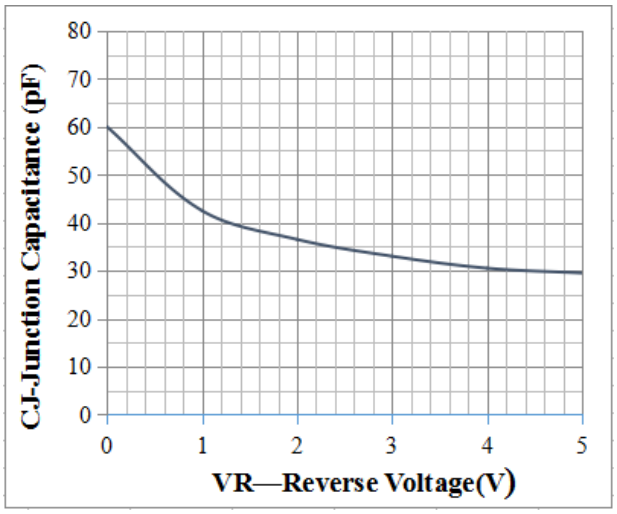
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Reverse Working Voltage	V_{RWM}				5	V
Breakdown Voltage	V_{BR}	I_T = 1mA	6.0		8.5	V
Reverse Leakage Current	I_R	V_{RWM} = 5V			0.2	uA
Clamping Voltage	V_C	I_{PP} = 8A (8 x 20μs pulse)			12	V
Junction Capacitance	C_J	VR = 0V, f = 1MHz			70	pF

Portion Electronics Parameter

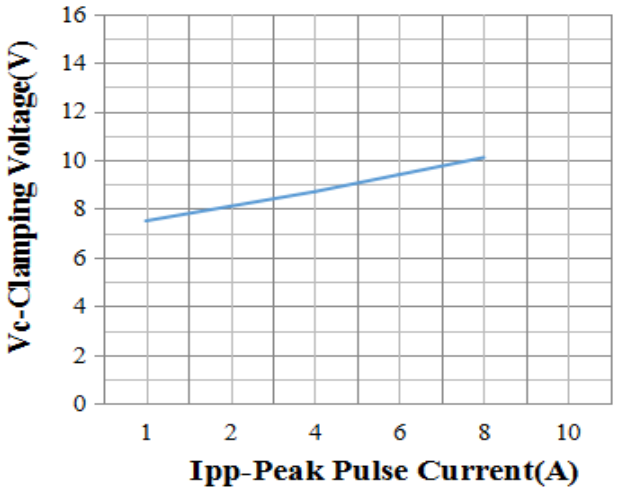
Symbol	Parameter
I_T	Test Current
I_{PP}	Maximum Reverse Peak Pulse Current
Vc	Clamping Voltage @Ic



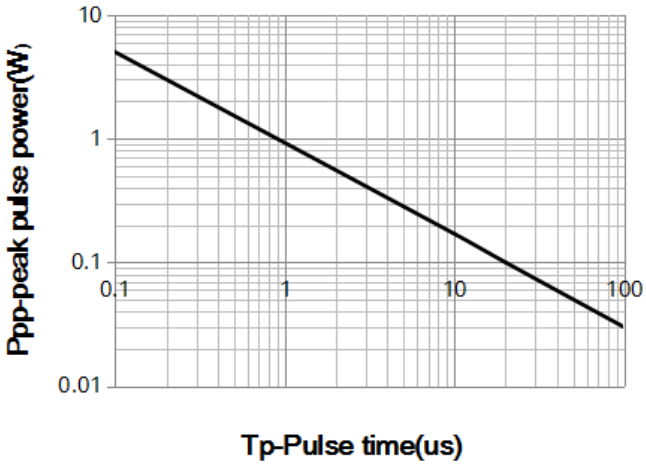
Typical Performance Characteristics (T_A=25°C unless otherwise Specified)



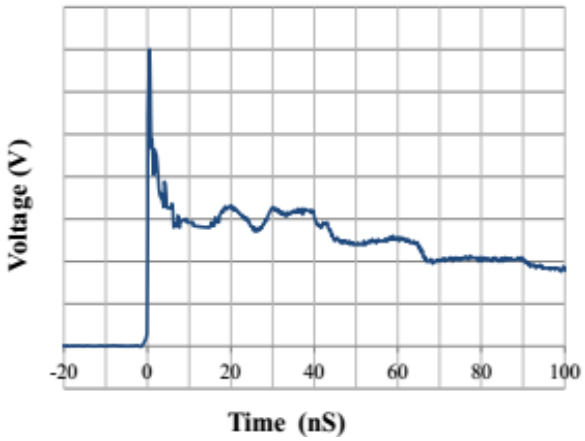
Junction Capacitance vs. Reverse Voltage



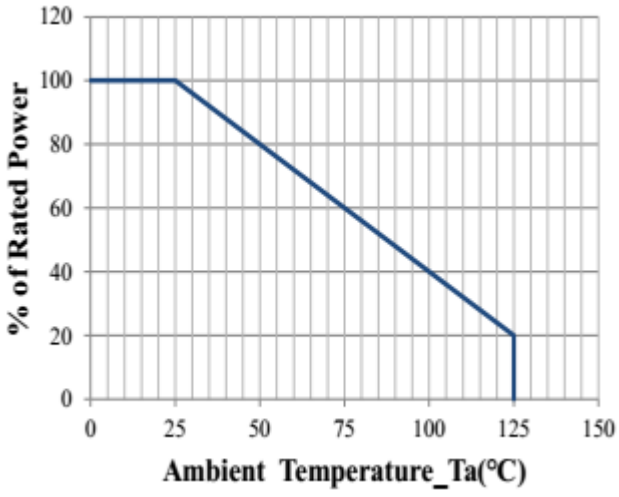
Clamping Voltage vs. Peak Pulse Current



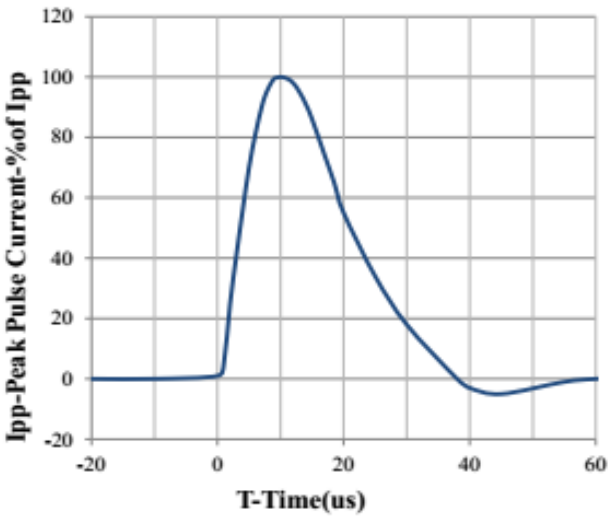
Peak Pulse Power vs. Pulse Time



IEC61000−4−2 Pulse Waveform

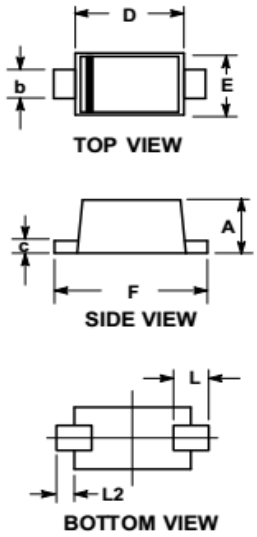


Power Derating Curve



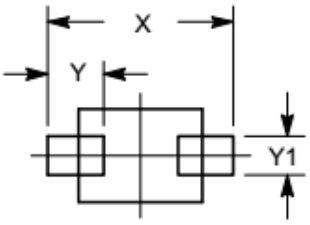
8 X 20us Pulse Waveform

SOD-923 Package Outline Drawing (Dimensions in millimeters)



SYM	DIMENSIONS					
	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.39	0.42	0.45	0.016	0.017	0.018
b	0.15	0.20	0.25	0.006	0.008	0.010
c	0.07	0.12	0.17	0.003	0.005	0.007
D	0.75	0.80	0.85	0.030	0.032	0.034
E	0.55	0.60	0.65	0.022	0.024	0.026
F	0.95	1.00	1.05	0.038	0.040	0.042
L	0.19 REF			0.007 REF		
L2	0.05	0.10	0.15	0.002	0.004	0.006

Suggested Land Pattern



SYM	DIMENSIONS	
	MILLIMETERS	INCHES
X	1.20	0.048
Y	0.36	0.014
Y1	0.25	0.010

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