



JLS18UGD5-3

1-Line Uni-directional High Power TVS Diode

Jialan-Microelectronics

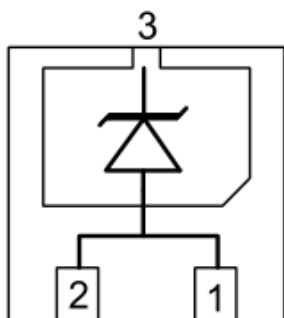
Description

The JLS18UGD5-3 is a high power TVS, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive lines.. It is assembled into a 3-pin DFN2020-3 lead-free package. The leads are finished with NiPdAu. Each device will protect one line. The combination of small size, and high surge capability makes them ideal for use in applications such as cellular phones, LCD displays, USB, and multi media card interfaces.

Features

- * 6500W peak pulse power (8/20μs)
- * Low leakage: nA level
- * Operating voltage: 18V
- * Ultra low clamping voltage
- * One power line protects
- * Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
 - Air discharge: ±30kV
 - Contact discharge: ±30kV
 - IEC61000-4-5 (Lightning) 180A (8/20μs)
- * RoHS Compliant
- * Package: DFN2020-3
- * Lead Finish: NiPdAu

Circuit Diagram

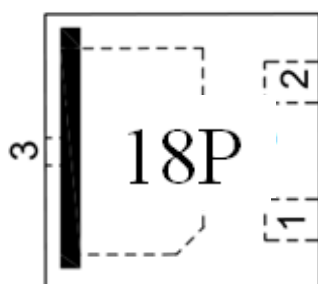


Transparent bottom view

Applications

- * Power Management
- * Industrial Application
- * Power Supply Protection
- * Many other portable devices

Marking Diagram



Transparent top view

18P:Device Marking Code
Bar denotes cathode

Ordering Information

Part Number	Packaging	Reel Size
JLS18UGD5-3	3000/Tape & Reel	7 inch



Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

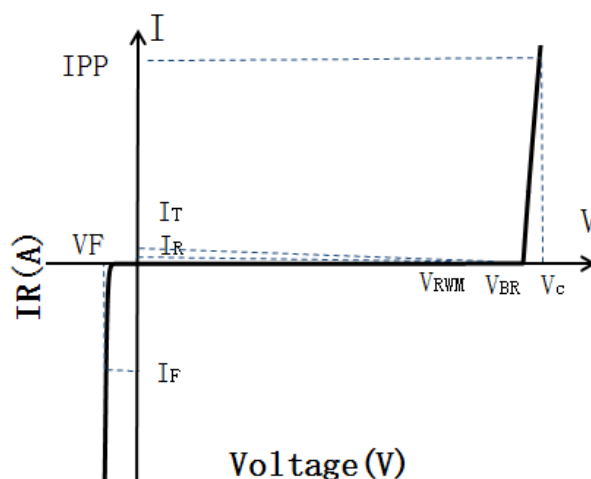
Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20 μs)	Ppk	6500	W
Peak Pulse Current (8/20 μs)	IPP	180	A
ESD per IEC 61000-4-2 (Air)	VESD	± 30	kV
ESD per IEC 61000-4-2 (Contact)		± 30	
Operating Temperature Range	TJ	-55 to +125	$^{\circ}\text{C}$
Storage Temperature Range	Tstg	-55 to +150	$^{\circ}\text{C}$

Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Reverse Working Voltage	V_{RWM}				18	V
Breakdown Voltage	V_{BR}	$I_T = 1\text{mA}$	19.0		22.0	V
Reverse Leakage Current	I_R	$V_{RWM} = 18\text{V}$			1.0	μA
Clamping Voltage	V_C	$I_{PP} = 100\text{A}$ (8 x 20 μs pulse)			34.0	V
Clamping Voltage	V_C	$I_{PP} = 180\text{A}$ (8 x 20 μs pulse)			43.0	V
Junction Capacitance	C_J	$V_R = 0\text{V}$, $f = 1\text{MHz}$		450		pF

Portion Electronics Parameter

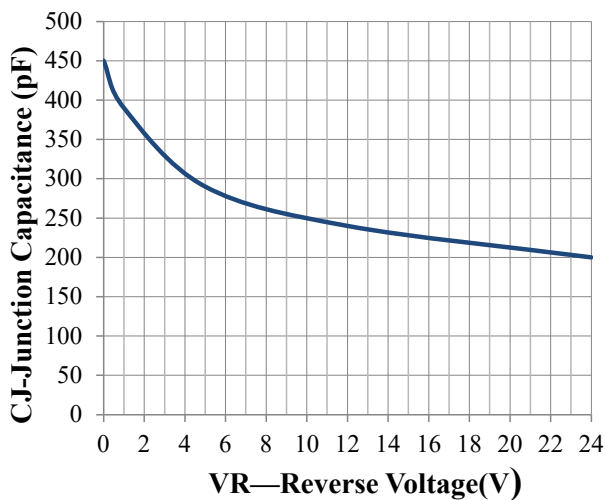
Symbol	Parameter
I_T	Test Current
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_C
I_F	Forward Current
V_F	Forward Voltage @ I_F



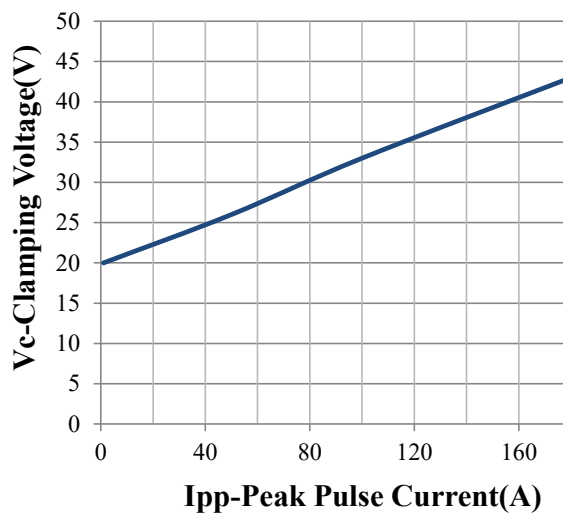


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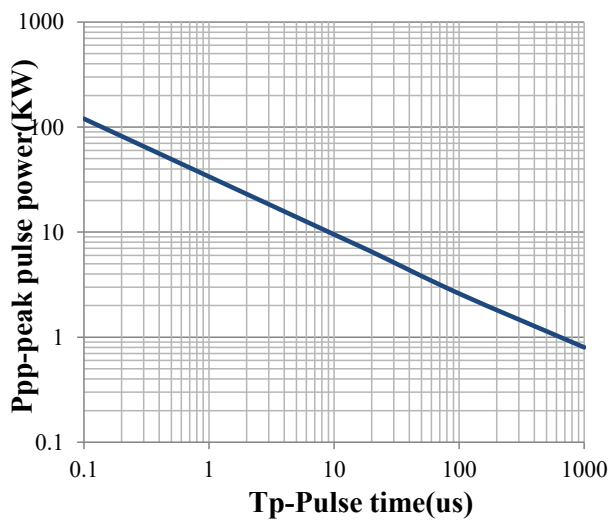
Typical Performance Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise Specified)



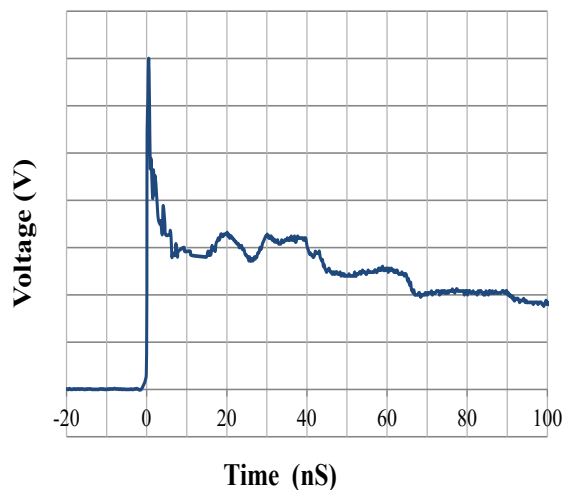
Junction Capacitance vs. Reverse Voltage



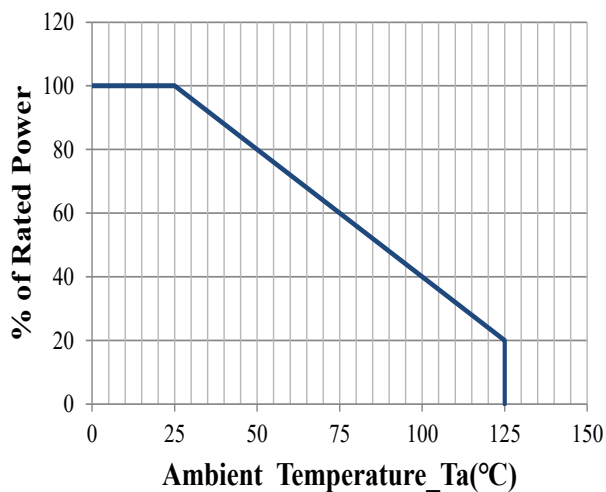
Clamping Voltage vs. Peak Pulse Current



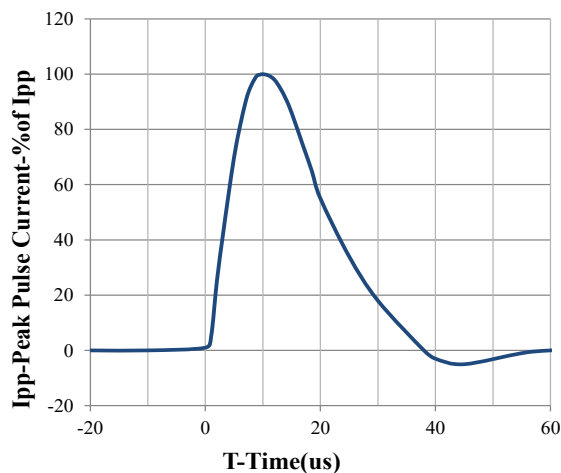
Peak Pulse Power vs. Pulse Time



IEC61000-4-2 Pulse Waveform



Power Derating Curve

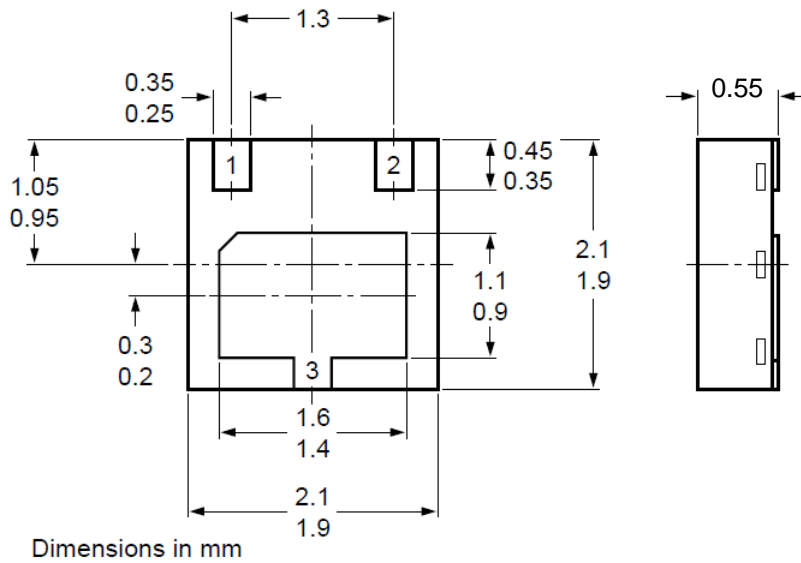


8 X 20us Pulse Waveform

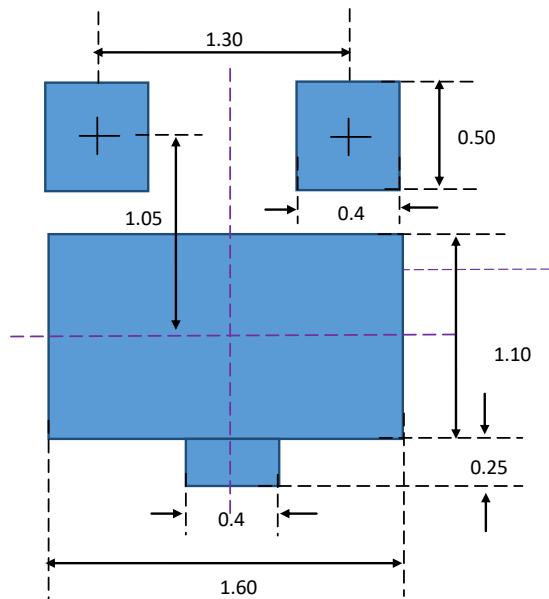


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DFN2020-3 Package Outline Drawing



Suggested Land Pattern



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