

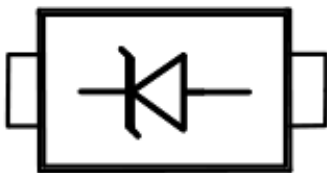


Description

The JLS07UGS1-2 is an uni-directional TVS diode, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive data and power lines.

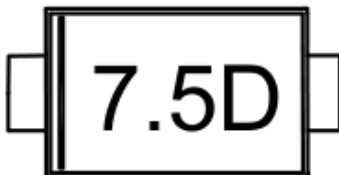
The JLS07UGS1-2 complies with the IEC 61000-4-2 (ESD) standard with $\pm 30\text{kV}$ air and $\pm 30\text{kV}$ contact discharge. It is assembled into a SOD-123FL lead-free package. The small size and high ESD/surge protection make JLS07UGS1-2 an ideal choice to protect cell phone, digital cameras, audio players and many other portable applications.

Circuit Diagram



Circuit and Pin Schematic

Marking Diagram



Transparent top view

7.5D:Device Marking Code

Bar denotes cathode

Features

- * 3500W peak pulse power (8/20 μs)
- * Low leakage:uA
- * Operating voltage: 7V
- * Ultra low clamping voltage
- * One power line protects
- * Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
 - Air discharge: $\pm 30\text{kV}$
 - Contact discharge: $\pm 30\text{kV}$
 - IEC61000-4-5 (Lightning) 170A (8/20 μs)
- * RoHS Compliant
- * Package: SOD-123FL
- * Lead Finish: Matte Tin

Applications

Fast-charge battery chargers
Power management system
Cellular Handsets and Accessories
Personal Digital Assistants
Notebooks and Handhelds
Portable Instrumentation
Digital Cameras

Ordering Information

Part Number	Packaging	Reel Size
JLS07UGS1-2	3000/Tape & Reel	7 inch



JLS07UGS1-2

Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise specified)

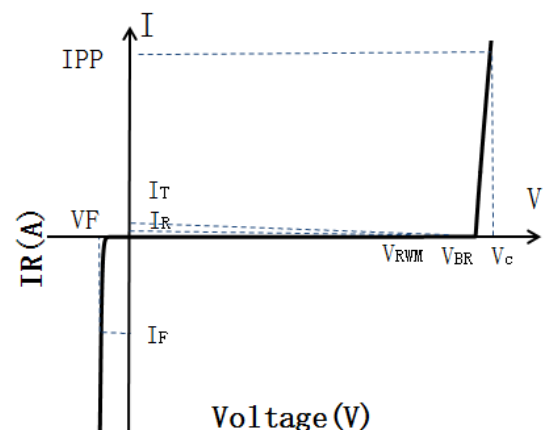
Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20 μs)	Ppk	3500	W
Peak Pulse Current (8/20 μs)	IPP	170	A
ESD per IEC 61000-4-2 (Air)	VESD	± 30	kV
ESD per IEC 61000-4-2 (Contact)		± 30	
Operating Temperature Range	TJ	-55 to +125	$^\circ\text{C}$
Storage Temperature Range	Tstg	-55 to +150	$^\circ\text{C}$

Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Reverse Working Voltage	V_{RWM}				7	V
Breakdown Voltage	V_{BR}	$I_T = 1\text{mA}$	7.5			V
Reverse Leakage Current	I_R	$V_{RWM} = 7\text{V}$			3.0	μA
Clamping Voltage	V_C	$I_{PP} = 100\text{A}$ (8 x 20 μs pulse)			16.0	V
Clamping Voltage	V_C	$I_{PP} = 170\text{A}$ (8 x 20 μs pulse)			20.5	V
Junction Capacitance	C_J	$V_R = 0\text{V}$, $f = 1\text{MHz}$		520	600	pF

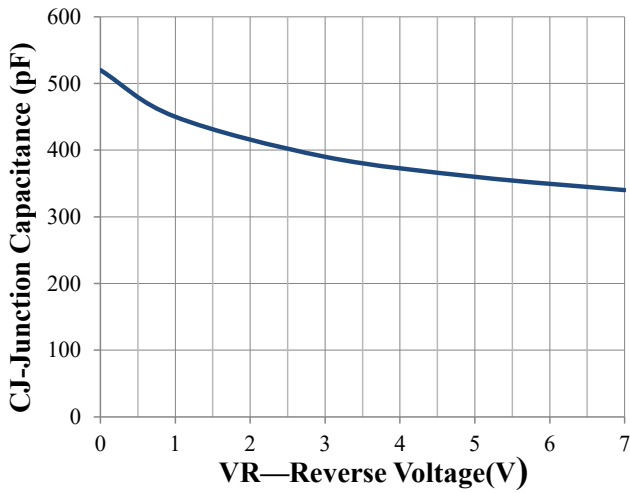
Portion Electronics Parameter

Symbol	Parameter
I_T	Test Current
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_C

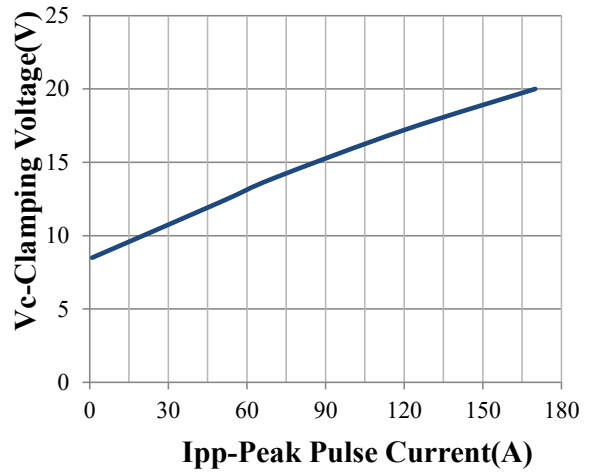




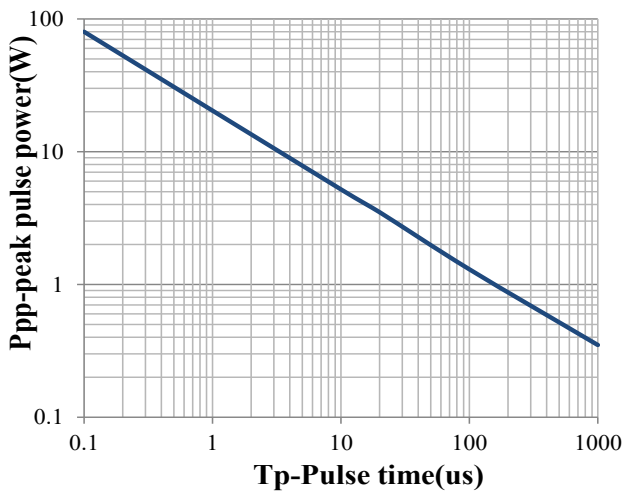
Typical Performance Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise Specified)



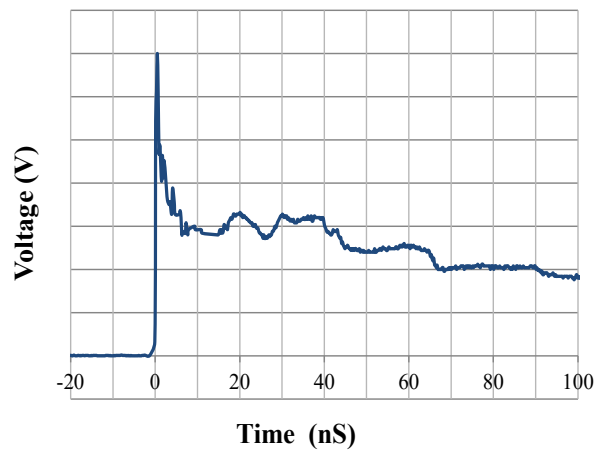
Junction Capacitance vs. Reverse Voltage



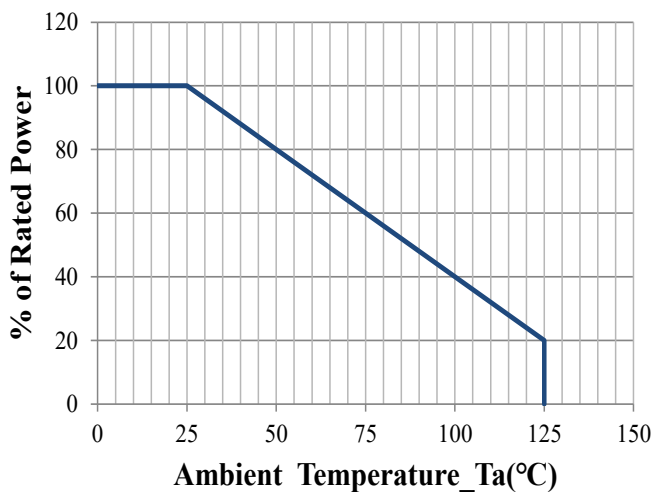
Clamping Voltage vs. Peak Pulse Current



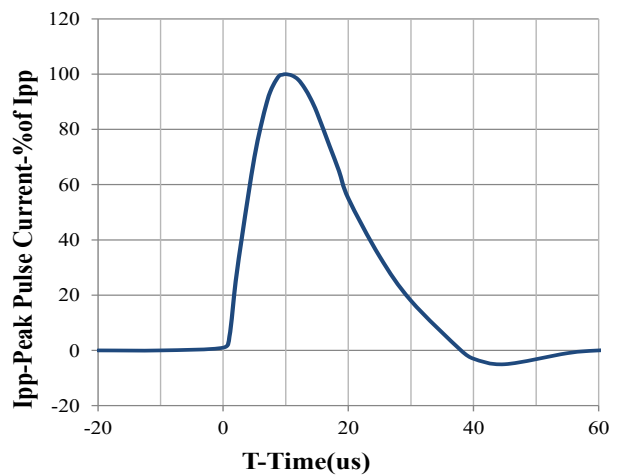
Peak Pulse Power vs. Pulse Time



IEC61000-4-2 Pulse Waveform



Power Derating Curve

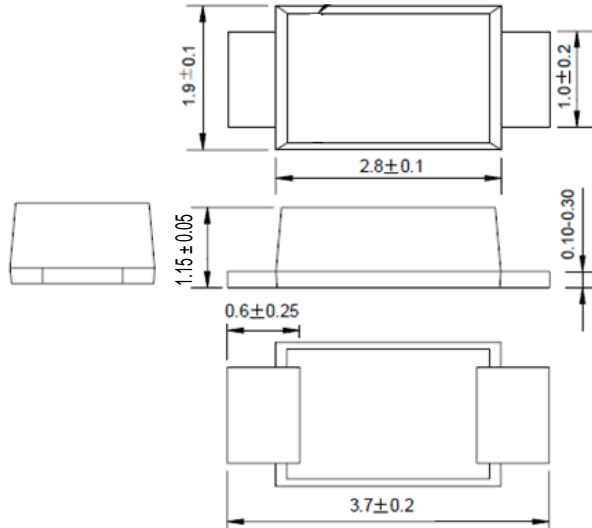


8 X 20us Pulse Waveform

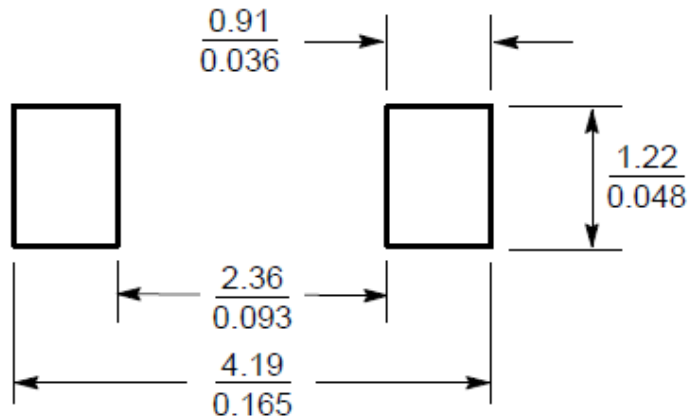


JLS07UGS1-2

SOD-123FL Package Outline Drawing (Dimensions in millimeters)



Suggested Land Pattern



SCALE 10:1 $\left(\frac{\text{mm}}{\text{inches}} \right)$

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